



Call for Papers and Plug-Fest

2006 Conference on IEEE-1588 Standard for a Precision Clock Synchronization Protocol for Networked Measurement and Control Systems

Sponsored by:

The National Institute of Standards and Technology

Hosted by:

The National Institute of Standards and Technology

Date: October 2-4, 2006

Location: The National Institute of Standards and Technology, Gaithersburg, MD, USA

Conference Co-Chairs: John Eidson, Agilent Technologies, john_eidson@agilent.com,

Kang Lee, NIST, kang.lee@nist.gov

Hans Weibel, Zurich University of Applied Sciences, wei@zhwin.ch

ABOUT THE WORKSHOP

The annual IEEE 1588 Conferences began in 2003. Each conference has featured presentations by industry, academic, and government representatives on a variety of topics related to IEEE 1588 technical specifications and issues, implementations and applications. The 2006 Conference will follow the successful format of the prior conferences, and will provide a forum for reporting on technical issues, product development, and application experience based on IEEE 1588. The conference will include a tutorial for newcomers and a plug-fest to explore the interoperability of IEEE 1588 implementations. This year's conference will be held at the NIST facility in Gaithersburg MD USA. The 2007 Conference will be held in Vienna. See <http://ieee1588.nist.gov> for additional details on IEEE 1588 and the proceedings of the 2003, 2004, and 2005 conferences. Details of the agenda, local arrangements, and registration will be posted on the web site as they become available. The proceedings of the conference will be published and archived as a NIST Technical Report.

We welcome paper submissions from engineers and scientists in industry, academia, and government reporting on all areas of interest to the IEEE 1588 community. Topics of interest include but are not limited to:

- Implementation design and performance issues
 - Analytic, modeling, and simulation studies
 - System and device level studies and system design issues
 - Management of IEEE 1588 systems
 - Mappings to non-Ethernet/UDP networks
 - Servo design for slave clocks
- Device design and performance reports on prototype or product implementations
 - Boundary & transparent clocks
 - End devices
 - Management tools
 - Components and embedded implementations
- Applications
 - Field trials

- Requirements for new applications
 - Status reports of existing projects
 - System integration issues
- Software
 - APIs
 - Object models
 - Programming models
 - Design environments and tools for applications
 - Performance test and evaluation tools

The plug-fest is an excellent opportunity to demonstrate device and system interoperability and to resolve compatibility issues. Implementers are invited and encouraged to cooperate with others to demonstrate system-wide clock synchronization and operation of the IEEE 1588 protocols across as wide a spectrum of devices as possible. Participants in the plug-fest will hold one or more phone meetings to determine plug-fest and demonstration criteria prior to the conference.

All paper submissions shall be of a scientific, technical, or application nature. Sales oriented submissions will be rejected.

Prospective paper authors should submit a one page extended abstract describing the paper's contents. Prospective participants in the plug-fest should submit a letter of intent to participate and an abstract of one page or less describing the devices or applications and indicate the technical issues illustrated by the submission. All abstracts and letters of intent should be sent to the conference co-chair John C. Eidson, john_eidson@agilent.com, by their respective due dates.

Important Due Dates:

Letters of intent to participate in plug-fest due June 23, 2006

Abstracts for papers due June 30, 2006

Acceptance notice for papers and plug-fest July 21, 2006

Final PowerPoint slides for presentations due September 22, 2006

Final written material to be published in the proceedings due October 2, 2006